## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Mail Stop RCE

Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

Re: Inventor(s): Derick G. Behrends et al.

Title: METHODS AND APPARATUS FOR TESTING INTEGRATED CIRCUITS

 Serial No.:
 10/636,060

 Filed:
 August 7, 2003

 Examiner:
 James C. Kerveros

Group Art Unit: 2117

Transmitted herewith is:

## X Request for Continued Examination (RCE)

FEE CALCU	JLATION				
Fee Items	Highest No. of claims previously paid	Present No. of claims filed	Extra Claims	Fee Rate	Total
Total Claims				X \$50.00	\$0.00
Independent Claims				X \$210.00	\$0.00
Basic Filing Fee (\$310.00), Utility Search Fee (\$510.00), Utility Examination Fee (\$210.00) \$1030.00					\$0.00
TOTAL FEES					PAID

- The Commissioner is hereby authorized to charge \$0.00 to Deposit Account No. 09-0465.
- XX The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. <u>09-0465</u>.

XX Please address all future correspondence to: Customer # 46628

Date: March 18, 2008 Respectfully submitted,

Brian M. Dugan Registration No. 41,720 (914) 579-2200